

System Level Power Workshop

What You Need to Know

Sponsored by:



Design Automation
Standards Committee



- 1:30pm **Welcome**
[John Ellis, Si2](#)
- 1:45pm **System Level Power Modeling Usage and Requirements**
[Vita Vishnyakov, Microsoft](#)
[Ahmad Ansari, Xilinx](#)
[Barry Pangrle, Consultant](#)
[Tom Quan, TSMC](#)
- 3:00pm **System Level Power Standardization**
[Stan Krolikoski, Chair, DASC LP Coordination Committee](#)
[Sushma Honnavara-Prasad, Secretary, IEEE P1801](#)
[Vojin Zivojnovich, Chair, IEEE P2415](#)
[Nagu Dhanwada, Chair, IEEE P2416](#)
[Jerry Frenkil, Member, Si2 Low Power Coalition](#)
- 3:45pm **Town Hall: Facilitated Discussion**
John Redmond, Broadcom
- 4:25pm **Closing Remarks**
Shishpal Rawat, Accellera